A SIMPLIFIED METHOD FOR SHORT CIRCUIT CALCULATIONS IN LV RADIAL NETWORKS

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Abstract

This paper develops a simplified approach for the calculation of three phase short circuit current in radial low voltage LV networks. This method is based on data available at design stage, such as, voltage drop in cables and maximum design currents. It is an effective method for construction engineers in the field of electrical installation for checking the dimensions of switchgears and other equipment to withstand the thermal and dynamic effects of short circuits in site conditions.

Keywords

Short Circuit, Low Voltage , Design Current, Voltage Drop.

1. Introduction

IEC 60781 (and BS 7638) presents an application guide for the calculation of short circuit currents in low voltage radial systems [1, 2]. Short circuit current is an important parameter for the selection of low voltage equipment that will be capable to withstand the thermal and dynamic effects of fault condition. Short circuit parameters are defined by this guide in terms, which include the following:

a. prospective (available) short circuit current.
b. Peak short circuit current.
c. Symmetrical short circuit breaking current.
d. Steady state short circuit current.

Significant effort has been made by engineers and academicians to improve and simplify the short circuit current SCC methods and compile the SCC standards and guidelines to be more suitable for industrial applications [3,4].

It is not always necessary to precisely calculate the short circuit current, and in some cases, a fast method is preferred to estimate the order of short circuit current, especially during construction activities.

In low voltage systems it is important to estimate or calculate the value of the prospective current likely to occur under short circuit conditions and to ensure that the devices provided to interrupt that current are rated to withstand and interrupt it.

There are many easy ways to calculate the short circuit current. Unfortunately as the calculating methods become easier, their accuracy gets less.

The proposed method is used to calculate the initial symmetrical three-phase short circuit current $I_k^s$, which is the rms value of the ac symmetrical component of a prospective short circuit current.

The low voltage system is a radial system, which consists of a MV/LV transformer and downstream cables and boards.

2. Assumptions

The following assumptions are made in developing this method:

a. The short circuit is far from generator and the low voltage system is supplied at one point only.
b. The low voltage system is unmeshed.
c. Reactances are ignored and if not ignored, the impedance of the element is the algebraic (not vectorial) sum of resistance and reactance.
d. Contributions from motors are ignored $\sum I_{rM} \leq 0.001 I_k^s$ .
e. Transformer tap changers are assumed to be in the main position.

3. The simplified method

This method is based on technical data available at the design stage, and namely the percent voltage drop and the maximum design current of each section.

The method of the equivalent voltage source at the short circuit location is applied for the calculation of short
circuit currents in low voltage systems [5]. In developing this method we will ignore the voltage correction factor \( c \). This factor basically increases the voltage magnitude of the voltage source applied to the passive network by 10%. This makes the fault current at least 10% higher and sometimes leads to more conservative results [6].

For three-phase short circuit,

\[
I_{pk}^* = \frac{V_r}{\sqrt{3}Z_{sc}} \quad (1)
\]

The per unit impedance is the ratio of the actual to base unit, thus

\[
Z_{pu} = \frac{Z_{actual}}{Z_B} \quad (2)
\]

And

\[
Z_B = \frac{V_B}{\sqrt{3}I_B} = \frac{V_r}{\sqrt{3}I_B} \quad (3)
\]

Where:

\( V_B \) - the base voltage, V
\( I_B \) – the base current, A.

Substituting Eq. (3) in Eq. (2), yields

\[
Z_{pu} = \frac{\sqrt{3}Z_{actual}I_B}{V_r} \quad (4)
\]

Or

\[
Z_{actual} = \frac{Z_{pu}V_r}{\sqrt{3}I_B} \quad (4-1)
\]

Now, if we consider that \( Z_{sc} \) in Eq. (1), which is in ohms is \( Z_{actual} \), then substitute \( Z_{actual} \) from Eq. (4) into Eq. (1), we obtain

\[
I_{pk}^* = \frac{I_B}{Z_{pu}} \quad (5)
\]

When designing low voltage networks, it is required to calculate the percent voltage drop for each cable by the following formula:

\[
\Delta V_\% = \frac{\sqrt{3}I_{max}Z}{V_r} \times 100\% \quad (6)
\]

Where:

\( \Delta V_\% \) - percent voltage drop, %.
\( I_{max} \) – maximum design current, A.
\( V_r \) – rated voltage, V.

dividing Eq. (3) on Eq. (6), yields

\[
I_{pk}^* = \frac{I_B}{Z_{pu}} \quad (5)
\]

Usually, the short circuit current calculations are performed in per unit system. The per unit is the ratio of

\[
\frac{Z_{pu}}{\Delta V_\%} = \frac{I_B}{I_{max} \times 100}\% \quad (7)
\]

Thus, the per unit impedance of a radial section consists of the per unit impedance of the transformer and the cables. Based on equation (7), the per unit impedance of the transformer is:

\[
Z_{pu} = \frac{Z_{pu}V_r}{\sqrt{3}I_B} \quad (8)
\]

Where:

\( Z_{pu} \) - percent voltage impedance of transformer, %.
\( I_{tr} \) – rated current of transformer, A.

And for the cable is:

\[
Z_{pu} = \frac{\Delta V_\% I_B}{100 I_{max} \times 100} \quad (9)
\]

Substitute Eqs. (8) and (9) in Eq. (5), we get

\[
I_{pk}^* = \frac{100}{\Delta V_\% + \frac{Z_{pu}V_r}{\sqrt{3}I_B}} \quad (10)
\]

Where:

\( I_{max} \) – maximum design current of the cable, A.

If the LV network consists of more than one cable, then Eq. (10) becomes:

\[
I_{pk}^* = \frac{100}{\Delta V_\% + \frac{Z_{pu}V_r}{\sqrt{3}I_{max} \times I_{rT}}} \quad (11)
\]

In an LV network the active resistance of the cables is known (or may be calculated), therefore the active component of the voltage drop may be calculated by the following formula:

\[
\Delta V_{ac\%} = \frac{\sqrt{3}I_{max}Z\cos\phi}{V_r} \times 100\% \quad (12)
\]

By dividing Eq. (6) on Eq. (12), we obtain:
\[ K_1 = \frac{\Delta V_{a\%}}{\Delta V} = \frac{Z}{Z \cos \varphi} = \frac{1}{\cos \varphi} \]  \hspace{1cm} (13)

Where:
- \( \Delta V_{a\%} \) – impedance of the cable.
- \( Z \cos \varphi \) – power factor.

In equation (13), \( K_1 \) depends only on the power factor \( \cos \varphi \):

Finally, we have

\[ I_k'' = \frac{100}{\sum \frac{K_1 \Delta V_{a\%} + z_{0b}}{I_{max} + I_{rT}}} \]  \hspace{1cm} (14)

Once \( I_k'' \) is known, then the following criteria must be considered for the short circuit strength of LV systems [7,8]:

\[ I_{cn} \geq I_k'' \]  \hspace{1cm} (15)

\[ I_{cm} = c I_{cn} = k \sqrt{2} I_{cn} \]  \hspace{1cm} (16)

Where:
- \( I_{cn} \) – the rated ultimate short circuit breaking current (breaking capacity).
- \( I_{cm} \) – the rated short circuit making capacity (making capacity).

4. Verification of the method

A simple radial section of a LV network is selected to verify the correctness of the proposed method. The section consists of MV/LV transformer, 185 mm\(^2\), 100 m cable, and 70 mm\(^2\), 50 m cable connecting a 100 KW motor to sub-distribution board (Fig. 1).

The calculations of three-phase short-circuit currents were performed by traditional equivalent voltage source and the proposed simplified method. The results of calculations are shown in table 1.

<table>
<thead>
<tr>
<th>( I_k'' ) (A)</th>
<th>Voltage source method</th>
<th>Simplified method</th>
<th>Error, %</th>
</tr>
</thead>
<tbody>
<tr>
<td>Main board</td>
<td>7429</td>
<td>7214</td>
<td>2.9</td>
</tr>
<tr>
<td>Sub-dis.board</td>
<td>4839</td>
<td>4576</td>
<td>5.4</td>
</tr>
</tbody>
</table>

It is obvious from the table that errors resulting from using this method is acceptable from engineering point of view.

Conclusions

It is important to note that the intention of this paper was to present a simplified method for the calculation of short circuit current in radial network, and to provide engineers who are designing LV network with a simple and easy tool to check the suitability of selected equipment in terms of switchgear dimensions and protection setting.

The essence of the method is to use data that are available at design stage such as voltage drop, maximum design current with transformer parameters such as voltage impedance and rated current.

The errors resulting from using this method are acceptable from engineering point of view.

References


